3032 3032	112	Subclass	ASSIFICATION	-			-	-	PATENT NUMBER
11000 U	13.	Class	ISSUE CLA		us ur	ILITY Paten	t Applicati	ion	
					SCANNED	O.I.P.E.		ATENT DATE	
APPLICA 09	TION NO /893			PRIOR F	CLASS 713	SUBCLASS	ART UNIT 2125	EXAMINER  M. I	LNOA
PLICANTS	aoyu	ki	Ogino	)		د ۶۱ ک			

Signal processing circuit

ISSUING CLASSIFICATION									
ORIGINA	\L	CROSS REFERENCE(S)							
CLASS	CLASS SUBCLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)						
711	112	360	7						
INTERNATIONAL	CLASSIFICATION								
606F12/	'08								
				☐ Continued on Issue Slip Inside File Jacket					

TERMINAL		DRAWINGS		CLAIMS ALLOWED		
└─ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
	2	2	2	6	1	
☐ The term of this patent	MIDYS INCA MIDYS SINCA Apaistam Examiner)		8/15/08 (Date)	NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.				<del>-</del>	`\ .	
☐ The term of this patent shall						
not extend beyond the expiration date of U.S Patent. No.				ISSUE FEE		
				Amount Due	Date Paid	
	(Primary E	xaminer)	(Date)			
☐ The terminalmonths of		•		ISSUE BATCH NUMBER		
this patent have been disclaimed.	(Legal Instruments Examiner) (Date					
WARNING:	-	_				
The information disclosed herein may be res Possession outside the U.S. Patent & Traden					5, Sections 122, 181 and 368.	
Form PTO-436A			EII ED WITH:	DINSK (CBE)	JEICHE JOPBON	

BEST AVAILABLE COPY